Form No.:	
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MSME-SAMSUNG TECHNICAL SCHOOL

REGISTRATION CUM ADMISSION FORM FOR ARISE-2 TRAINNING PROGRAMME

(PLEASE FILL IN USING BLOCK LETTERS AND PUT $\sqrt{\rm MARK}$ IN THE BOX)

NΑ	ME OF THE PROGRAM		art Phone etc. (Handheld Products-H ne Theater (Audio Video-AV) m Air Conditioner-RAC)	Paste your Iatest passport size photograph
			ine, Microwave Oven etc (Home App	oliances-HA)
DU	JRATION	FROM:		ŕ
1.	First Name :	Middle Name		
	Father's Name:		Last Name	
3.		: D D M M	Y Y Y	
4.	Gender	: Male Fe	emale	
5.	Category	: GEN OBC	SC ST	
6.	Physically Challenged	d: YES NO		
7.	Whether Minority If Yes, Minority Type	: YES NO		
8.	Educational Qualifica	tion: i) ITI Disciplin	ne	
		ii) Diploma Disciplin	ne	
		iii) Bsc. Disciplin	ne	
		iv) B-Tech Disciplin	ne	
		v) Others Discipline)	
9.	UID(AADHAR CARD	NO.):		
10	. Address Details :			
		/Town: -	District:-	
	Stat		Pin :-	
11	. Mobile/Phone No.: (Compulsory)		Email:-	

12. Fee Type :	Self Sponso	red Course Fee in	Rs
13. Present Status:	Self Employed	Wage Employed	Unemployed
14. Employer's Addre (If employed)	ess:		
15. Preferred Work Lo (City Name Only)	, -	ii)	iii)
best of my know	clare that the information wledge & belief.	on given in this application i	is true and complete to the
Place: Date:		Signature	e of the Applicant
For Office use only		<u>_</u>	••
ROLL No.:			BATCH No.:



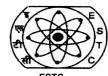
MSME-SAMSUNG TECHNICAL SCHOOL

MSME-DI Extension Centre

Bal Sahoyog (Opp. L.Block) Connaught Place New Delhi - 110001 H.O. – ELECTRONICS SERVICE & TRAINING CENTRE KANIYA, RAMNAGAR, DISTT. NAINITAL-244715 (U.K.)

REGISTRATION FORM

(To be filled by the Candidate)



A GOVT. OF INDIA SOCIETY 011- 23412611, 05947-251294

Name of the Course & D)ate				
 Name of the applican 	t				
2. Father's Name					
3. Date of Birth	4. S	Sex			
5. Postal Address :-					
	Din	Mak			
6. Category whether SC	/ST/General	IVIOL)		
o. Gatogory Whothor Go	701700110141 <u> </u>				
7. Name of the sponsori	ng organisation (i	f any)			
9 Amount deposited De		/ Pan	Nroft No	dt	
 Amount deposited Rs Academic / Profession 	oal qualification :	/-Dai	IK DIAIL NO	dt	
SI. Name of the Exam		Subjects	Name of the	Name of the Unive	areity /
No. passed	Obtained	Subjects	School/College		FISILY /
10. Professional Experie	ence (if any)				
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11. UNDERTAKING:					
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postpond I shall abide					
cancelled, the fee depo					
other course in the eve		the course /	leaving the course	in between due to p	ersonal
reasons or act of indiscip	pline.				
List of Encl.					
Sign. of Candidate			Training Co	-ordinator/Director (Ti	raining)
Mr. /Ma		OR OFFICE U		ha	
Mr./Ms admitted to the cours					
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Rs					
Cash receipt No					
Account.				or depositing into the	, 2010
J.O.A./O.A. (Training)					
A				-to at Discot	
Accounts Officer			Prin	icipal Director	

PROGRAMME TITLE: PLC Programming

DURATION: 04 Weeks

ELIGIBILITY : Degree/Diploma in Engg.

(Completed / Undergoing)

CONTENTS: Introduction to PLC, Need & Advantage, PLC Hardware Structure (CPU 224) & Operating Principles, Siemens (CPU 224) Hardware installation and wiring, Digital & Analog I/O Addressing, Master Software - Simatic Manger, Step-7 Micro win 32, PC to PLC Communication, Uploading / Downloading of User Program, PLC Programming Languages, Basic Logic Operations, Comparison & Arithmetic Operations, Flags, Timers and counters, Linear and structured Programming, Word Logic Operations, MMI Programming

Software - Simatic Manager, Step-7 Micro win 32

Hardware - PLC trainer kits

PROGRAMME TITLE: VLSI Design

DURATION: Four Week

ELIGIBILITY: Degree/Diploma in Engg. (Completed / Undergoing)

CONTENTS: Introduction to VLSI, Devices used in VLSI, VLSI Design Flow, PLD's and ASIC's, Comparison between PLD's, ASIC's and FPGA's,

Field Programmable Gate Arrays: Introduction, Basic elements of FPGA, Types of FPGA's, Advantages / Disadvantages of FPGA's, FPGA- ASIC hybrid, FPGA packaging, Vendors, Reading a FPGA IC, Front end and back end system designing, Front end designing using FPGA, VHDL and its advantages.

VHDL: Language Elements: Introduction, Identifiers and Keywords, Data objects and Data types, VHDL operators, VHDL design units.

VHDL: Behavioral Modeling: Introduction, Process statements, Components of a process statement, Delay in signal assignment.

VHDL: Structural Modeling: Introduction component declaration and Instantiation.

VHDL: Dataflow Modeling: Introduction, Concurrent signal assignment statement.

VHDL: Subprograms, Packages and Libraries - Introduction, Subprograms, packages, design libraries, Commonly used VHDL packages.

FPGA Design Flow: Introduction, FPGA based design flow, Programming and simulation with Xilinx ISE (Demo), Pin package assignment and generating bit file (demo), Configuring a Spartan 3 board (demo).

Practical: Basic gates (AND, OR, NAND, NOR, XOR, XNOR etc.) (Data flow), Half adder and full adder (dataflow), Multiplexer (4:1) using case statement (Behavioral), Encoder (4:2) using if statement (Behavioral), Implementing AOI circuits (Structural), JK flip flop (Asynchronous - Behavioral), JK flip flop (Synchronous - Behavioral), D flip flop (Synchronous - Behavioral), D flip flop (Synchronous - Behavioral), Serial in Serial out Shift register (Behavioral), 8 bit up down counter (Behavioral).

PROGRAMME TITLE : AUTOCAD

DURATION: 04 Weeks

ELIGIBILITY: Degree/Diploma in Engg.

(Completed / Undergoing)

CONTENTS :

Auto CAD 2014 - Two dimensional drawing, Modifying the drawing, Dimension, Format, Edit, Modeling.

Different type of layers, limit, polygon, polyline, construction line, helix, ellipse, donut, mirror, chamfer & match properly.

Three Dimensional drawing, 3D basic property & dimension, Hide, Shade & Rendering, Make six view, Dimension & complete drawing sheet with title block, learning & cylinder, cone, sphere, pyramid.

Make assembly of different parts.

Practical Training - Practical on 2D, 3D, solid work model, drawing, assembly & "e"mail drawing, explain & make drawings for lathe & milling 2 dimensional & 3 dimensional.